

Docket No.: 8733.721.10-US  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Hae Jin YUN

Confirmation No.: Unassigned

Application No.: TBA

Group Art Unit: TBA

Filed: August 20, 2003

Examiner: TBA

For: THIN FILM TRANSISTOR ARRAY  
SUBSTRATE AND MANUFACTURING  
METHOD THEREOF

Customer No.: 30827

**CLAIM FOR PRIORITY**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

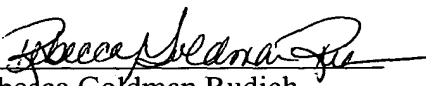
Applicant hereby claims priority under 35 U.S.C. 119 based on the following prior foreign application filed in the following foreign country on the date indicated:

| <u>Country</u> | <u>Application No.</u> | <u>Date</u>    |
|----------------|------------------------|----------------|
| Korea          | 2002-21057             | April 17, 2002 |

In support of this claim, Certified copies of the corresponding Convention Application(s) were filed in prior application Serial No. 10/274,879, filed October 22, 2002.

Dated: August 20, 2003

Respectfully submitted,

By   
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**30827**

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